

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Prior Application: J. ETOH et al
Serial No. 09/095,101
Filed: June 10, 1998

Group Art Unit: 2818
Examiner: S. Mai
For: LARGE SCALE INTEGRATED CIRCUIT
WITH SENSE AMPLIFIER CIRCUITS FOR
LOW VOLTAGE OPERATION

PRELIMINARY AMENDMENT

Assistant Commissioner of Patents
Washington, D.C. 20231

Sir:

Prior to examination, please amend the above-identified application as follows:

IN THE SPECIFICATION

Please amend the specification as set forth below.

Page 1, before the first line of the specification please insert the sentence:

This application is a continuation application of U.S. Serial No. 09/095,101, filed June 10, 1998, which is a reissue of 08/104,508, filed on 08/10/93 PAT. 5,525,313, which is a continuation of 07/366,869, filed 06/14/89 U.S. Pat. 5,247,077.

IN THE CLAIMS

Cancel claims 1-52, and add new claims 53-74 as follows.

53. A semiconductor device comprising:

a circuit block including a first node and a second node for receiving an operating voltage and a plurality of complementary MISFETs, each having a p-channel MISFET and an n-channel MISFET connected in series between the first node and the second node,